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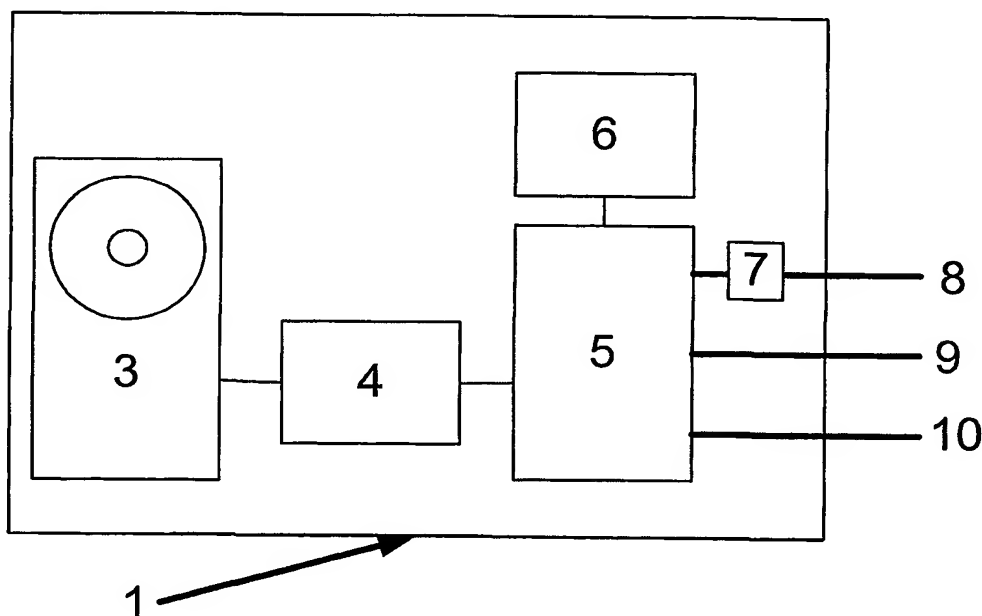
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(54) Title: **MODULE FOR READING A DATA CARRIER**



(57) Abstract: A module for reading a data carrier, with a processor arrangement and a memory arrangement, - wherein the module is designed for incorporation in a data processing device, and - wherein the processor arrangement is designed for storing an identification information associated with the data carrier and at least a start information in the memory arrangement when the reading of the data carrier is interrupted.

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